

SW1 1 2 SW2  
GDO0 3 4 GDO2  
RESET 5 6 VREG\_EN  
FIFO 7 8 EIFO

H2

LCDCAP 1 2 P5 7  
P5.6 3 4 P5 5

H5

P6.0 1 2 P6.1  
P6.2 3 4 P6.3  
P6.4 5 6 P6.5  
P6.6 7 8 P6.7

H8

UTXD1 1 2 IURXD1  
P4.2 3 4 SIMO1  
SOM1 5 6 IUC1K1  
P4.6 7 8 P4 7

H3

P7.0 1 2 IUC0SIMO  
UCA0SOM1 3 4 IUC0CLK  
P7.4 5 6 P7 5  
P7.6 7 8 P7 7

H6

VEREF- 1 2 VREF+  
P5.0 3 4 P5 1  
P10.6 5 6 P10.7

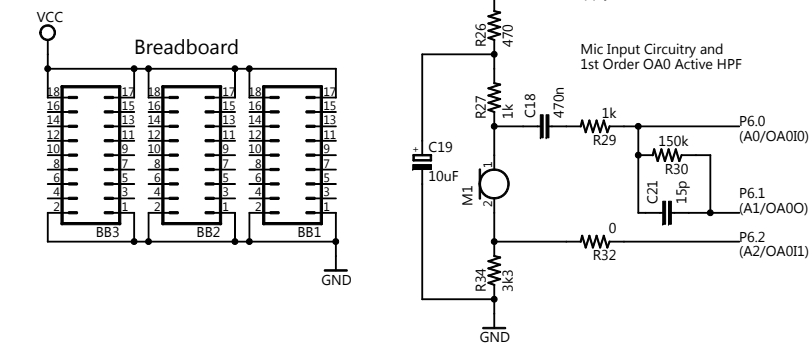
H9

P2.0 1 2 P2 1  
P2.2 3 4 P2 3  
UCA0TXD 5 6 IUC0RXD  
P2.6 7 8 P2 7

H4

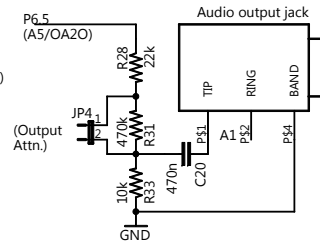
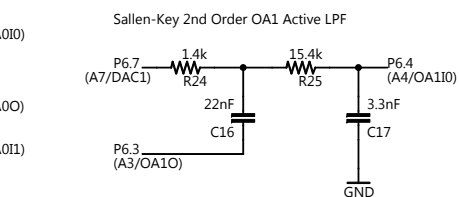
P3.0 1 2 IUCB0SDA  
UCB0SCI 3 4 IUCB0CLK  
P3.4 5 6 P3 5  
P3.6 7 8 P3 7

H7



VCC1: FG4618 Supply Config  
VCC2: F2013 Supply Config

Pos 1-2: FET Powered  
Pos 2-3: Battery Powered



SoftBaugh SBLCDA4			
S0	P514	1A.1B.1C.1D	
S1	P513	1F.1G.1E.DP1	
S2	P512	1A.2B.2C.2D	DOL_ERR_MINUS_MEM
S3	P511	2F.2E.2D.2P2	ENV_TX_RX_8BC
S4	P510	3A.3B.3C.3D2	ANT_A2_A1_A0
S5	P59	3A.3B.3C.3D	BT_B1_B0_BB
S6	P58	3F.3G.3E.COL3	AU_AR_AD_AL
S7	P57	4A.4B.4C.4D	PL_P0_P1_P2
S8	P56	4F.4G.4E.DP4	F1_F2_F3_F4
S9	P55	5A.5B.5C.5D	F5_PR_P4_P3
S10	P54	5F.5G.5E.COL5	COM0
S11	P53	6A.6B.6C.6D	COM1
S12	P52	6F.6G.6E.DP6	COM2
S13	P51	7A.7B.7C.7D	COM3
		7F.7G.7E.DP7	
			P526 S21
			P525 S20
			P524 S19
			P523 S18
			P522 S17
			P521 S16
			P520 S15
			P519 S14
			P518 COM0
			P517 COM1
			P516 COM2
			P515 COM3

Sheet: 1/1